

## **„Introduction in Parallel Programming and Optimization for Intel Xeon Phi Architecture”, Training at IICT-BAS, 7 July 2016**

This training is organized by VI-SEEM project and it is devoted to parallel programming with MPI and Open MP , access to the HPC infrastructure at IICT-BAS, as well as software techniques needed for supercomputers with highly dense parallel architectures, both homogeneous (Intel Xeon) and hybrid with acceleration co-processors(Intel Xeon Phi).

Expected audience: End-users from the three VI-SEEM communities – Life Science, Cultural Heritage and Climate, as well as PhD students and young researchers.

The training will be held in the IICT-BAS building (Bulgaria, Sofia, Acad. G. Bonchev, Bl.25A, Room 105).

### **Programme**

<b>Start Hour</b>	<b>End Hour</b>	<b>Description</b>	<b>Lector/ Location</b>
07 July 2016			
09:30	10:00	Registration	2 <sup>nd</sup> floor, IICT
10:00	10:30	VI-SEEM project: Regional Infrastructure and Scientific Communities	Aneta Karaivanova, 218 room
10:30	11:15	HPC Infrastructure at IICT-BAS and deployed software	Emanouil Atanassov, 218 room
11:15	11:30	Coffee break	2 <sup>nd</sup> floor, IICT
11:30	12:30	Hands-on: Access to HPC cluster - Avitohol	Mariya Durchova, Dimitar Dimitrov, 105 room
12:30	13:30	Lunch	2 <sup>nd</sup> floor, IICT
13:30	14:15	Introduction to parallel programming with MPI and OpenMP	Gergana Bencheva, 105 room
14:15	15:15	Hands-on: Parallel Programming with MPI and OpenMP	Mariya Durchova, Dimitar Dimitrov, 105 room
15:15	15:30	Coffee break	2 <sup>nd</sup> floor, IICT
15:30	16:30	Software techniques for optimization using Intel Xeon Phi coprocessors - presentation and hands-on.	Emanouil Atanassov, 105 room
16:30	16:40	End of training	